

#### SCOPE OF ACCREDITATION TO ISO/IEC 17025:2017

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#### **ELECTRICAL**

Valid To: February 28, 2026 Certificate Number: 2797.11

In recognition of the successful completion of the A2LA evaluation process, accreditation is granted to this laboratory to perform the following types of tests on <u>integrated circuits and electronic components</u>:

Test(s):	Test Method(s) <sup>1,2</sup> :	
Electrical		
Voltage Stress		
Rise / Fall Time (2 to 10) ns Rise / Delay Time (130 to 170) ns	JEDEC JS-001; JESD22-A114; Mil Std 883 TM 3015;	
Pin Capacity: 100 V to 8 kV Current: (0.06-5.86)A)	AEC-Q100-002; AEC-Q101-001; SOP61, WIF1 Test – Human Body Model	
Frequency (11 to 16) MHz Pin Capacity: 100V to 400V	JEDEC JESD22-A115; AEC-Q100-003, AEC-Q101-002; SOP61, WIF1	
Current: (1.5 to 8.0) A	Test – Machine Model	
I-Test Vsupply Over-Voltage Test	JEDEC JESD78; AEC-Q100-004; SOP61, WIF1 Test –IC Latch-Up	
Rise / Fall Time < 400 ps  Pin Capacity: 125V to 1 kV  Peak Current (1.4 to 27.9) A	JEDEC JESD22-C101, JEDEC JS-002; AEC-Q100-011, AEC-Q101-005; SOP61, WIF1 Test – Field Induced; Charged Device Model	

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Test(s):	Test Method(s):
<b>Environmental</b>	
Thermal Stress	
HTOL	Mil Std. 883TM
(High Temperature Operating Life)	1005, 1006, 1015
85 to 150 °C ± 3 °C	JESD22-A108
65 to 150 C ± 5 C (1 to 20) V / (0 to 500) A	SOP52, WIF20
HTSL	JESD22-A103
(High Temperature Storage Life)	SOP52, WIF15
100 to 185 °C	
THB	JESD22-A101
(Temperature Humidity Bias)	SOP52, WIF10
30 °C to 85 °C (60 to 95% RH Non-Condensing)	
PPOT – Pressure Pot	JESD22-A102
121 to 135 °C, 20 PSI, 100% Saturation	SOP52, WIF16
HAST (Highly Accelerated Stress Test)	JESD22-A110
110 + 145 0G 25 DGL 100 0 / G + 0 05 0 / DH	SOP52, WIF13
110 to 145 °C, 35 PSI, 100 % Saturation @ 85 %RH (Max)	
TMCL – Temperature Cycling	JESD22-A104;
	MIL-STD 883 TM 1011
Condition A-N (air to air)	SOP52, WIF7
(-65 to 150) °C	
10 min Dwell Instantaneous Ramp;	
5 min Dwell 15 min Ramp	
PTC – Power and Temperature Cycling	JESD22-A105
	SOP52, WIF8
Grade 1 and 2 materials	
Preconditioning Level 1 - 6	JESD22-A113
	SOP52, WIF12

<sup>&</sup>lt;sup>1</sup> Failure analysis performed using test methods listed. <sup>2</sup> SOP's are accredited internal methods.



A2LA has accredited

## EUROFINS EAG ENGINEERING SCIENCE, LLC

Santa Clara, CA

for technical competence in the field of

### **Electrical Testing**

This laboratory is accredited in accordance with the recognized International Standard ISO/IEC 17025:2017 General requirements for the competence of testing and calibration laboratories. This accreditation demonstrates technical competence for a defined scope and the operation of a laboratory quality management system (refer to joint ISO-ILAC-IAF Communiqué dated April 2017).



Presented this 25th day of March 2024.

Mr. Trace McInturff, Vice President, Accreditation Services For the Accreditation Council

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Revised March 28, 2024